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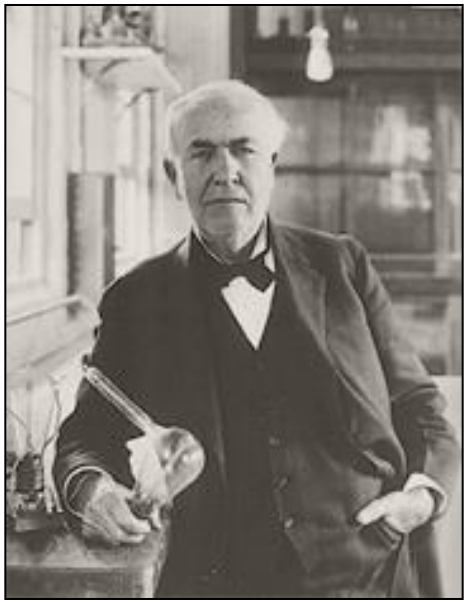

Thomas A. Edison

Alexander Graham Bell

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1963

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＋

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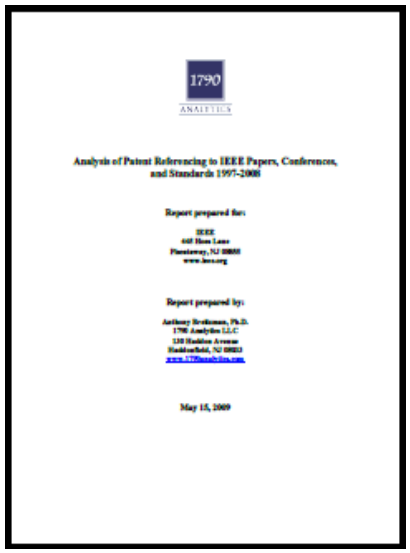
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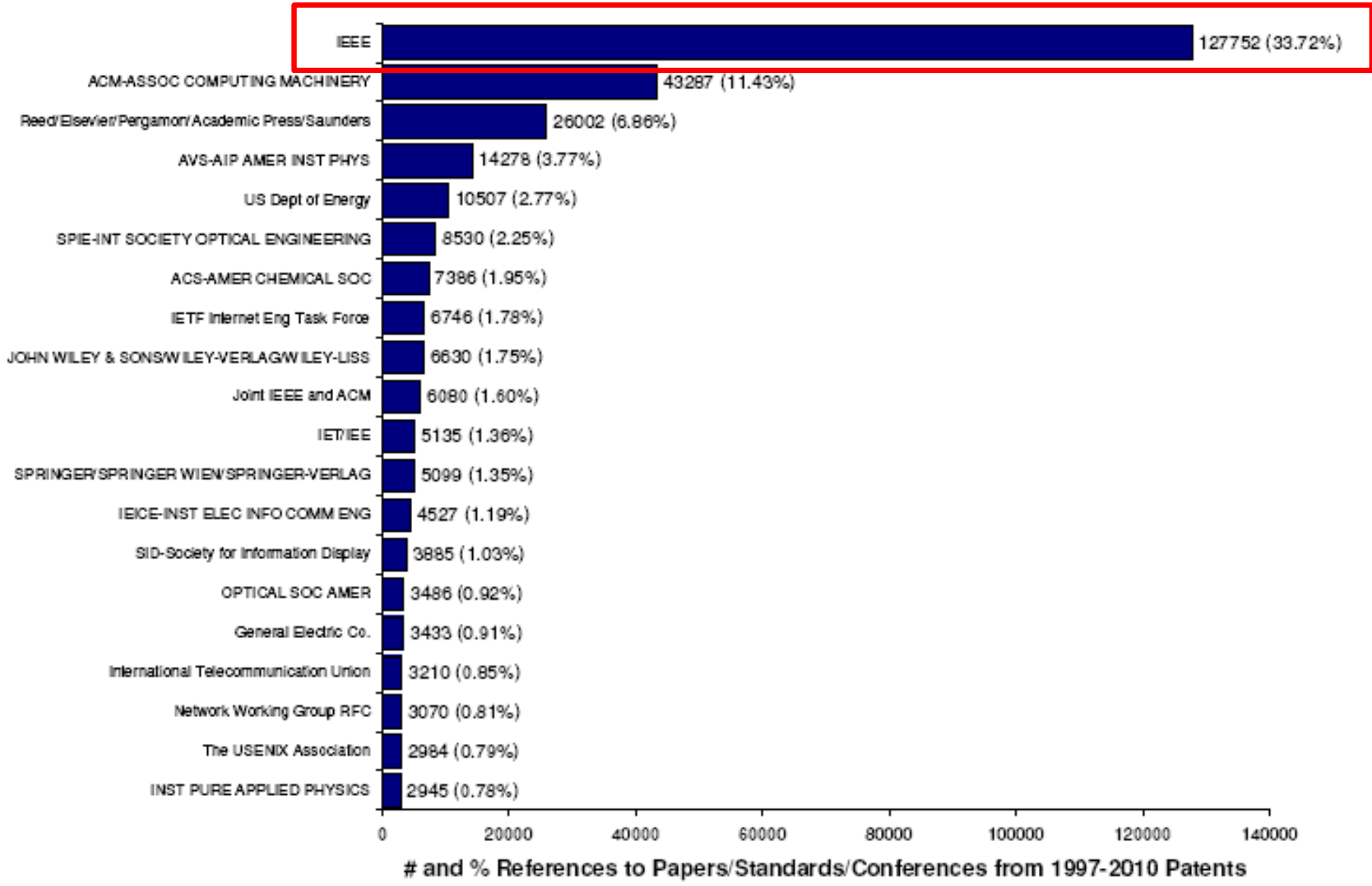

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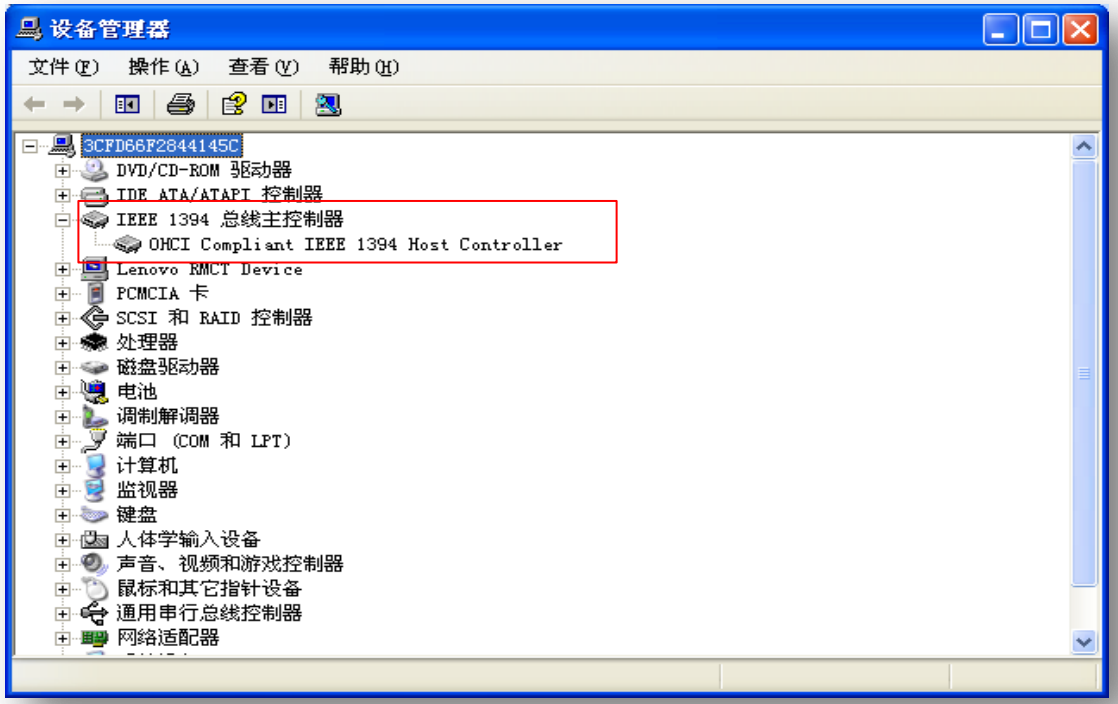

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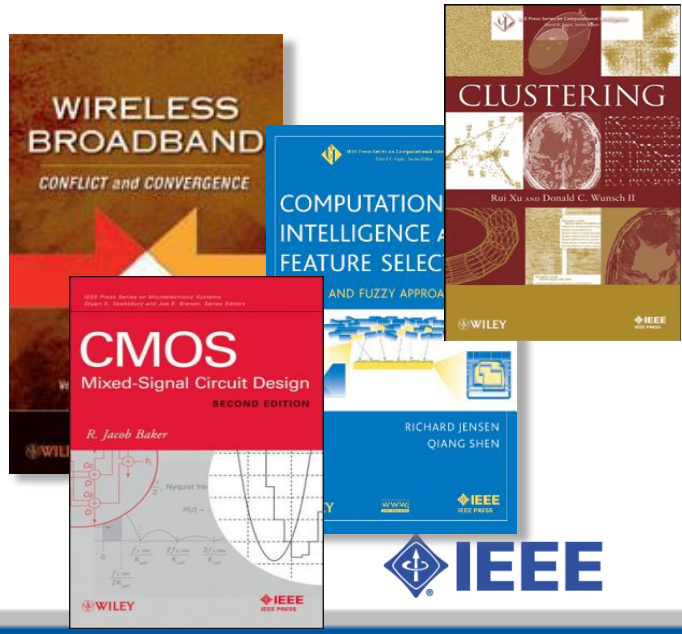

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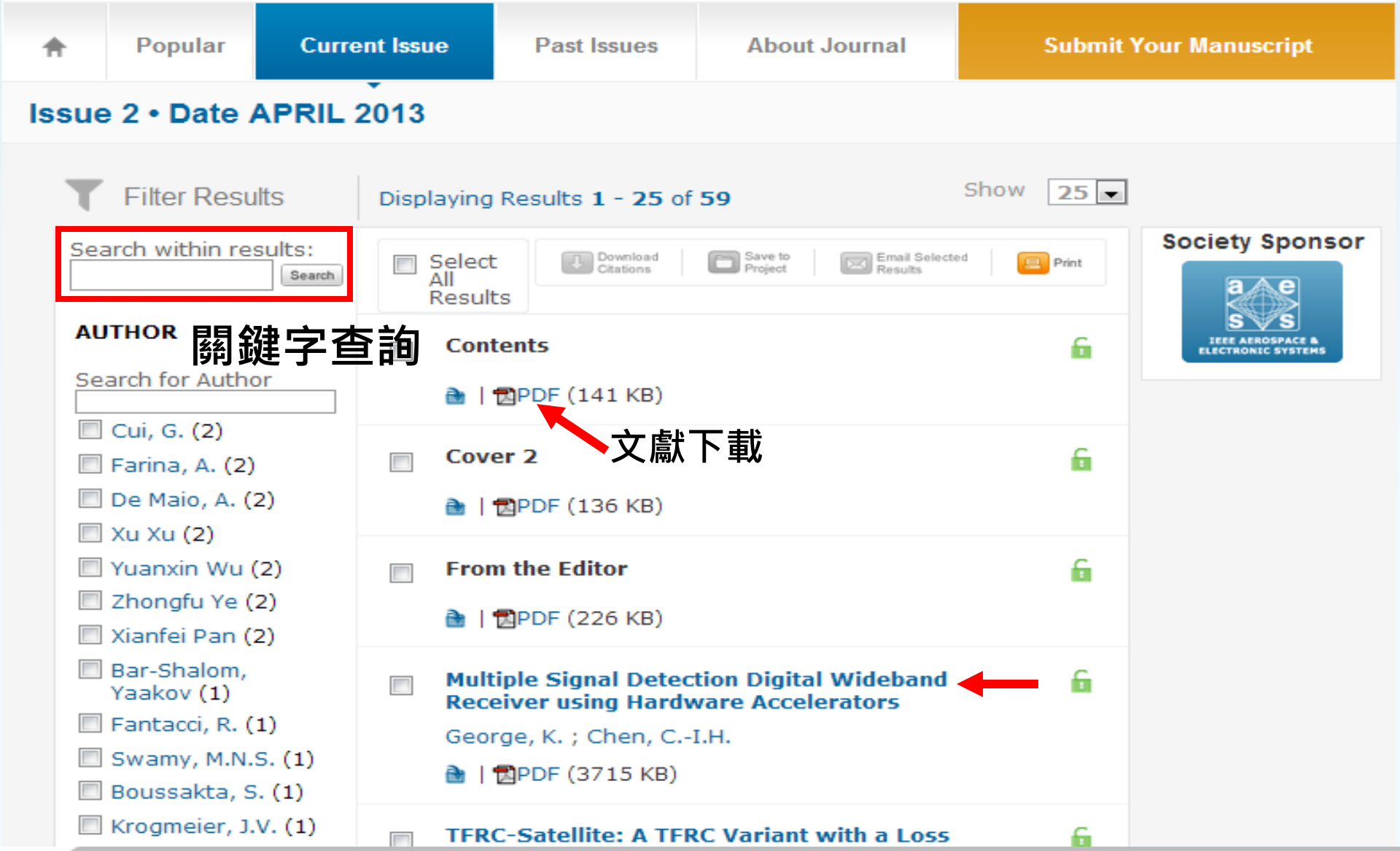

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Multiple Signal Detection Digital Wideband Receiver using Hardware Accelerators

KIRAN GEORGE
California State University
CHIEN-IN HENRY CHEN
Wright State University

A three gigasample per second (GSPS) digital wideband receiver that operates in a 1.25-GHz instantaneous bandwidth (IBW) is proposed. In addition to building such systems, offloading of computation-intensive tasks to a combination of specialized hardware accelerators such as graphics processing units (GPUs) and field-programmable gate arrays (FPGAs) to increase the overall receiver's dynamic performance is analyzed. The receiver detects up to 15 signals with a maximum attainable instantaneous dynamic range (IDR) of 62.5 dB before the next set of data arrives for processing.

I. INTRODUCTION

A modern radar receiver is required to intercept radio frequency (RF) signals and rapidly search a large frequency range with maximum sensitivity in real time to identify and locate the source. Furthermore, detection of incoming signals of any modulation, carrier frequency, and bandwidth is central to the working of a modern radar receiver, unlike the narrowband communication receivers, which are usually designated for known signals.

Unlike most radar receivers in the past that were designed using analog technology, digital radar receivers down convert the input RF signals to intermediate frequency (IF) signals and digitize them using high-speed analog-to-digital converters (ADCs) to retain all data information so as to produce better results than the analog counterpart [1]. Furthermore, the performance of digital receivers are superior in comparison to the conventional analog approach due to advantages such as 1) the ability to extract more signal information in the digital approach, 2) the ability to store digital data for long periods, and 3) the ability to use more advanced and flexible signal processing techniques for a digitized signal. The use of digital channelization techniques allows for improving imbalance and mismatch between the filters, which is a main challenge as seen in development of analog receivers.

The ADC is a critical subsystem in the digital radar receiver, especially for receivers that use direct

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The environmental performance criteria of the IEEE 1680 family of standards are intended to define a measure of environmental leadership in: the design and manufacture of ~~personal computer~~ electronic products ~~that are marketed to institutions~~; the delivery of specified services that are associated with the sale of the product ~~to institutions~~; and in associated corporate performance characteristics.

This family of standards is defined with the intention that the criteria are technically feasible to achieve, but that only products demonstrating the leading environmental performance currently available in the marketplace would meet them at the time of their adoption. As the environmental performance of products that are available in the marketplace improves, it is intended that the criteria will be updated and revised to set a higher performance standard for leadership products.

This standard is intended to serve as a baseline for further environmental standards for additional electronic products to be developed in the future. References to IEEE Std 1680 likewise reference, unless otherwise specified, the individual product standards in the IEEE 1680 family of standards.

1.3 Application

The environmental performance criteria are contained in the standards that are members of this IEEE 1680 family of standards. The principles and procedures identified in Clause 1 apply to ~~notebook personal computers, desktop personal computers, and personal computer monitors. The principles and procedures identified in Clause 1, Clause 2, and Clause 3 apply to personal computer~~ electronic products and will apply to future standards developed for additional electronic products.

Different configurations of a product, as defined in the standards in this family, may include options for processors, memory, hard disks, etc. A product, for the purpose of this family of standards, is every configuration that could be offered in a specific marketing model and chassis type. If ~~there is a~~ specific ~~configuration within a marketing model and chassis type that would change~~ configurations do not meet the ~~environmental performance substantially, especially if that configuration would no longer meet a criterion~~ criteria as declared, then the manufacturer ~~could not claim conformance to this Standard for that configuration, even if the same model in other configurations did conform to this Standard. The manufacturer~~ shall clearly report ~~such special~~ to the Product Registration Entity which configurations ~~that~~ do not ~~conform to~~ meet the ~~Standard to the Product Registration Entity~~ criteria as declared.

A product includes ~~a desktop computer, a notebook computer or monitor,~~ an electronic product and all the peripherals that are integral to its operation. For example, the desktop computer together with the keyboard, the mouse, and the power cord would be a product.

1.4 Conformance with the IEEE 1680 family of standards

A manufacturer may designate one or more specific geographic regions, or countries, for which a product declaration is applicable. In order to ~~conform to this Standard~~ be conformant in a designated region or country, each unit of ~~a~~ the product that is sold in that region or country must satisfy all of the applicable

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Topics: Engineering Profession ; General Topics for Engineers (Math, Science & Engineering)

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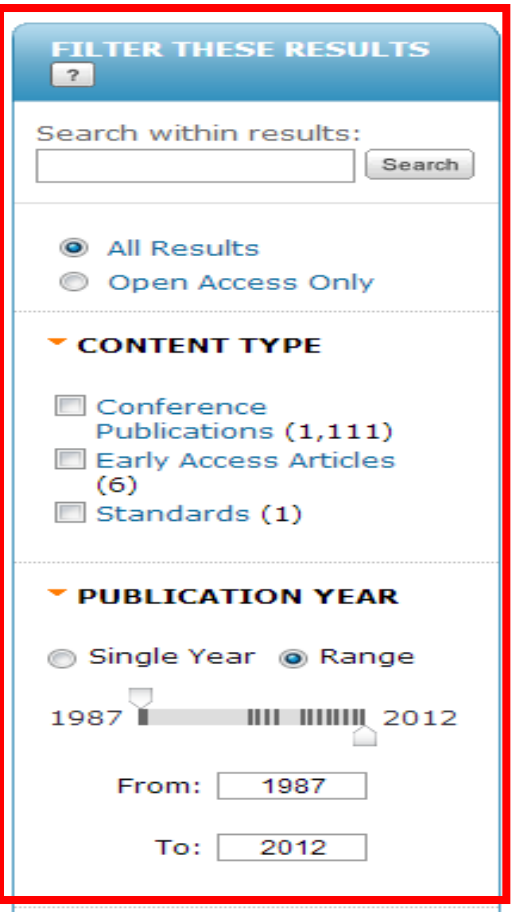

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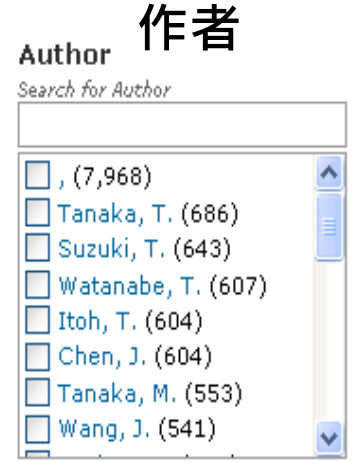

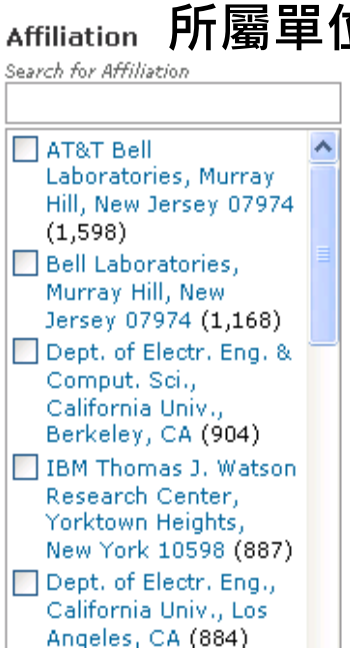

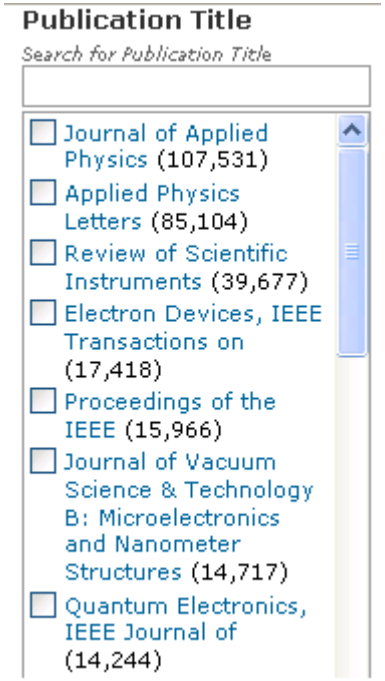

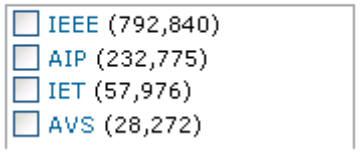

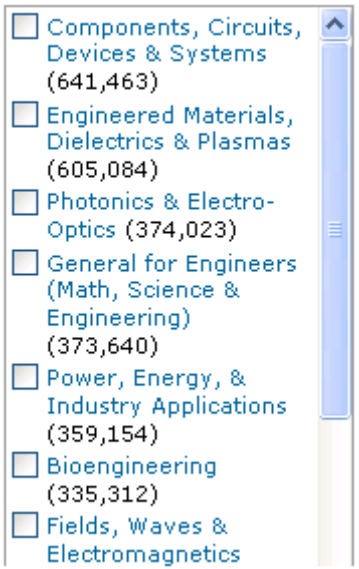

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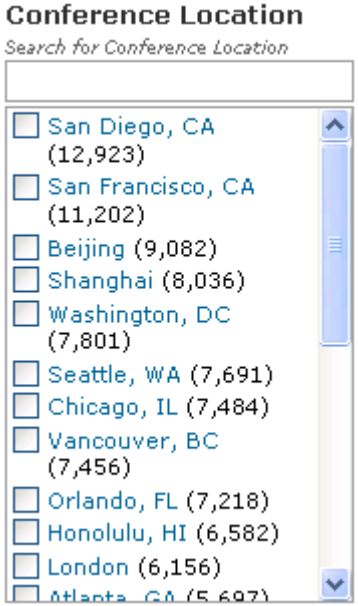

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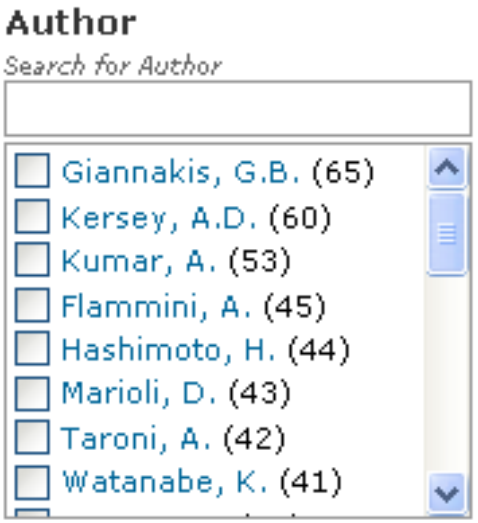

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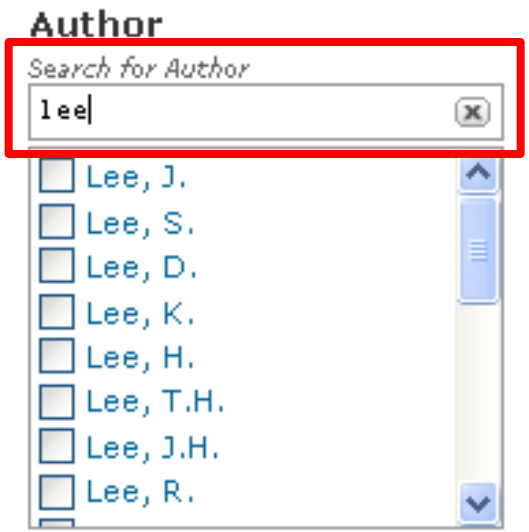

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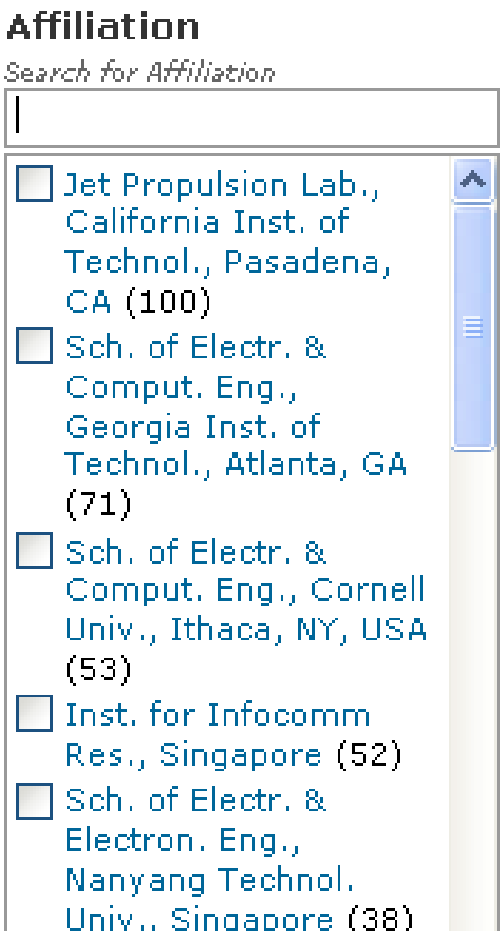

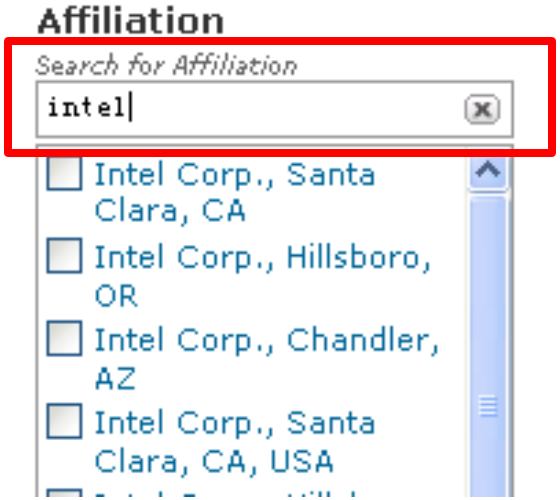

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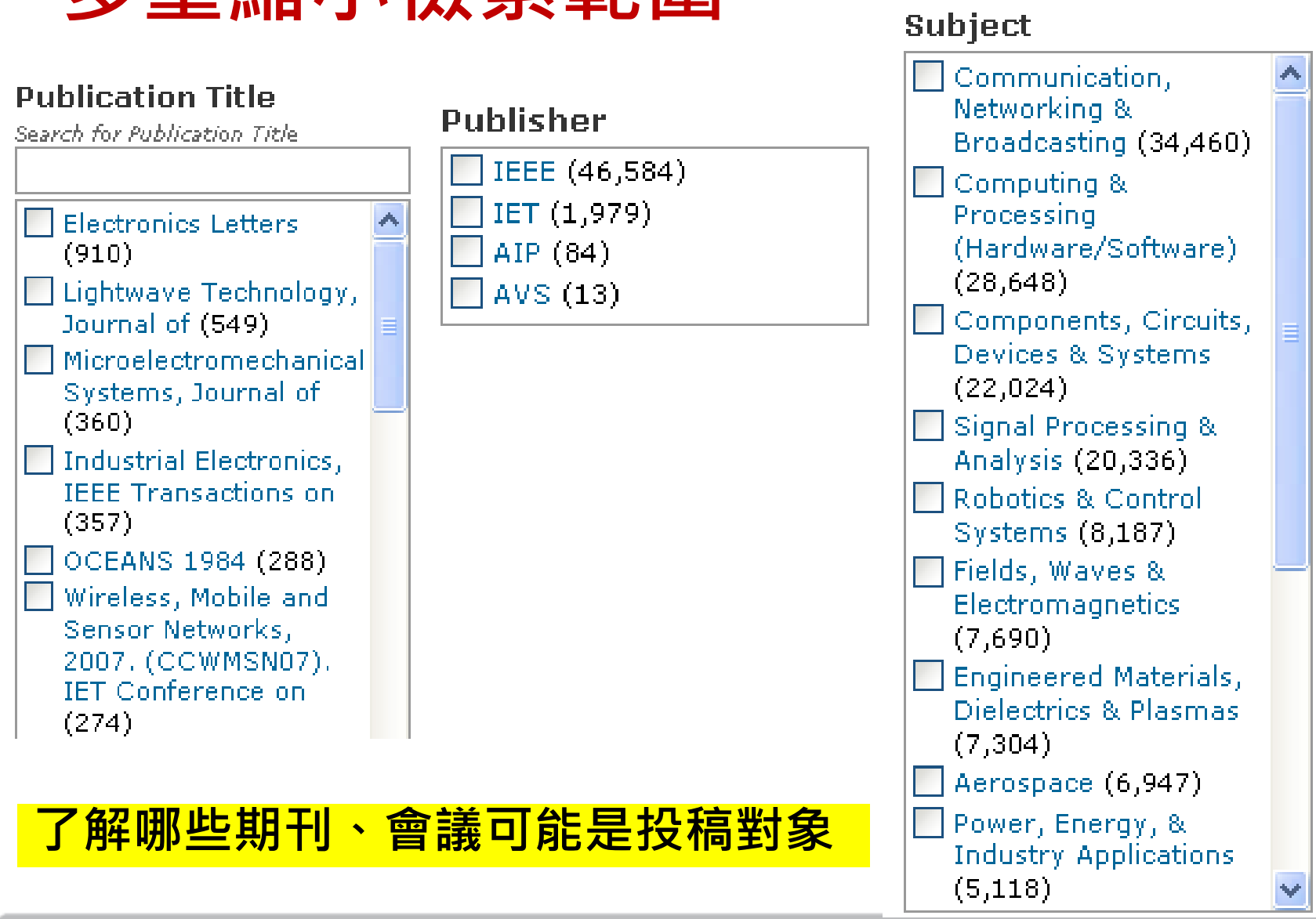

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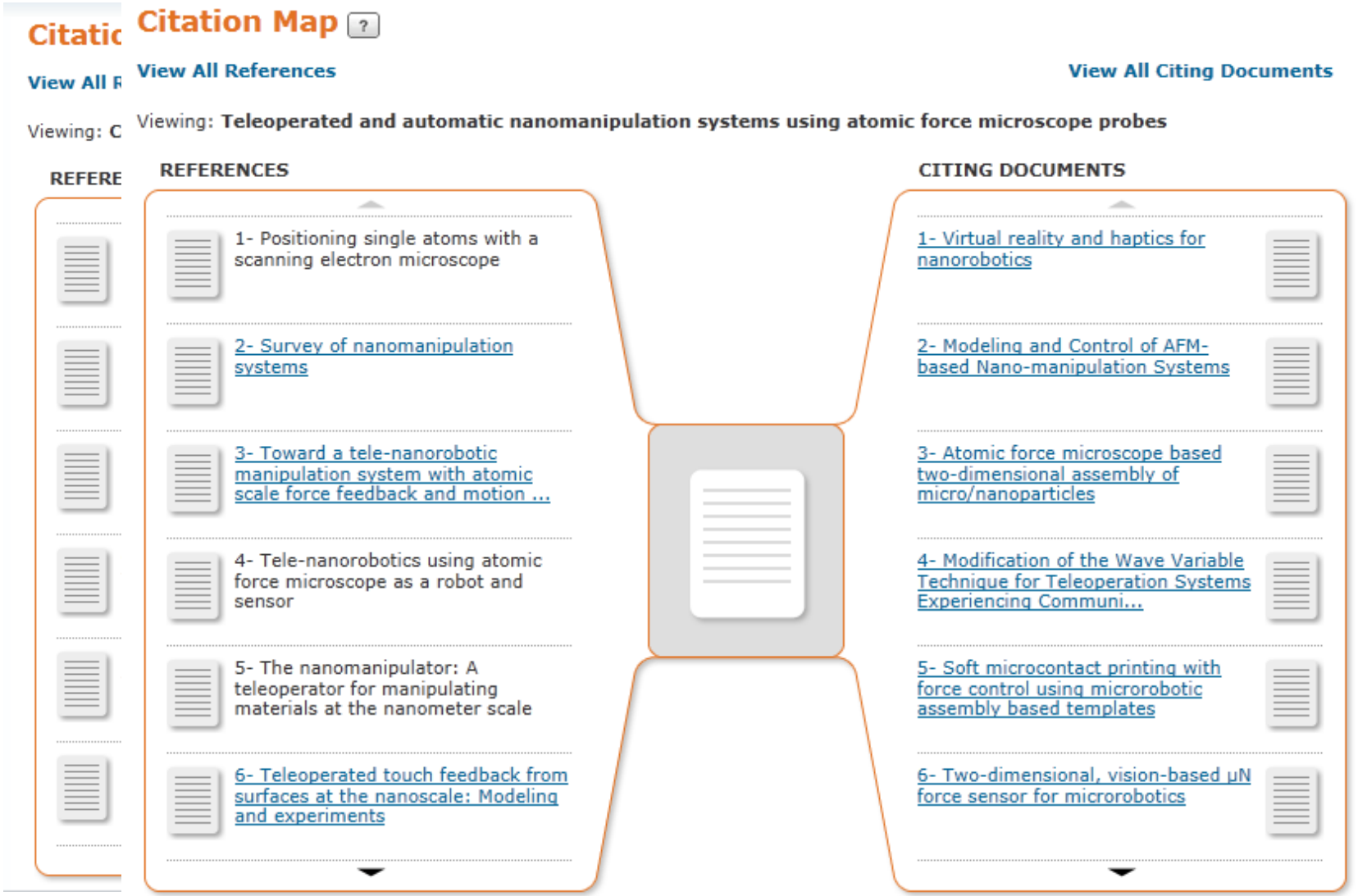

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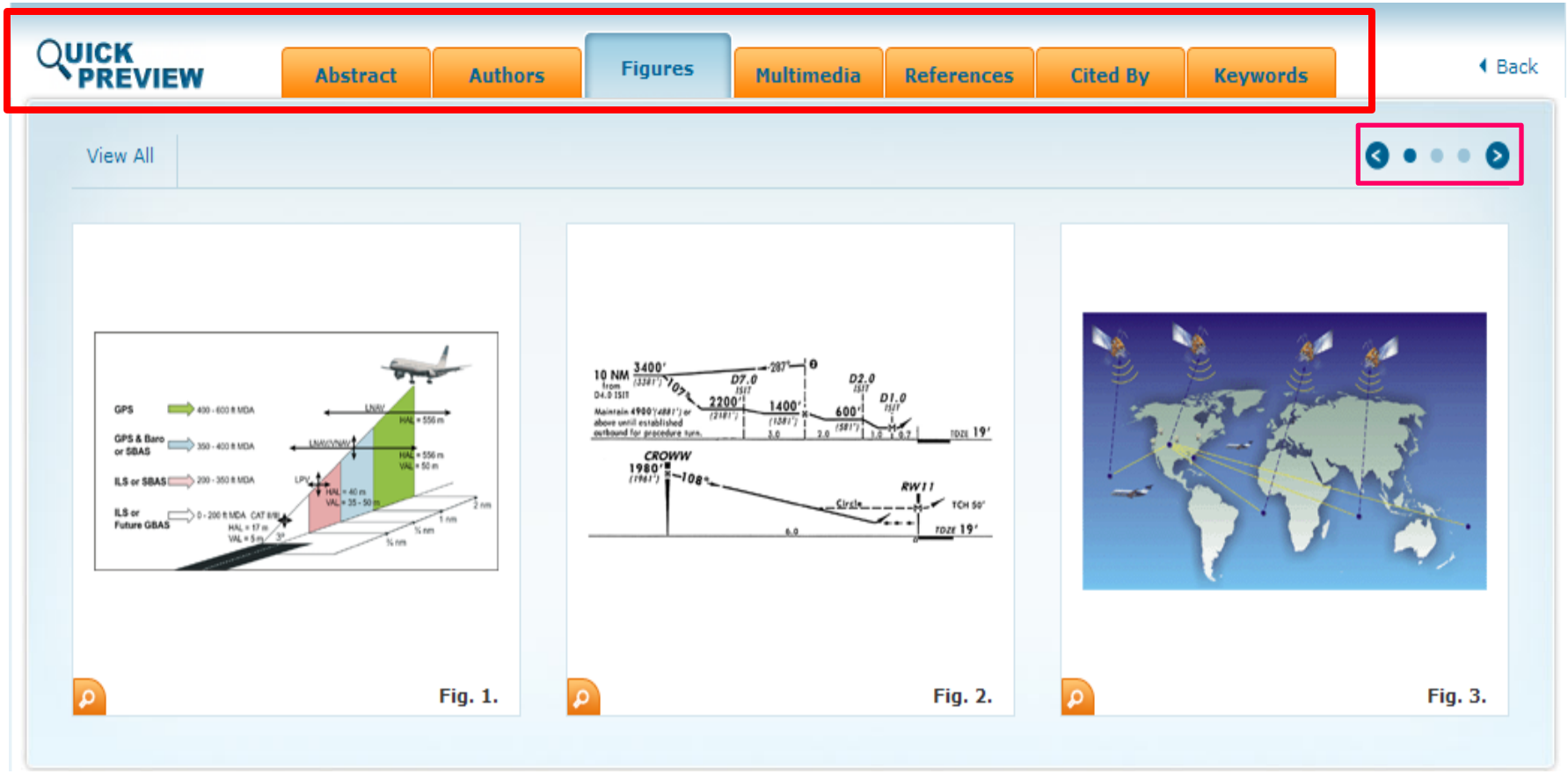
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For aircraft navigation, fault detection and isolation is paramount. Faults that may cause HMI must be noticed and mitigated in real-time. The operation of GPS has been very reliable and clearly reflects the extraordinary skill of the Air Force personnel that operate and maintain GPS. However, faults have occurred. Some are man-made and others are due to Mother Nature.

For example, the navigation data broadcast by the GPS satellites occasionally contain significant errors. As shown in Fig. 3, the GPS satellites are monitored by a relatively sparse ground control network (five stations are shown in Fig. 3 but eight new stations have been recently added [5]). Measurements at the ground stations are used to predict the orbit of the GPS satellites. These predictions are uploaded to the satellite and broadcast to the users. Generally, this estimated orbit is within 1 or 2 m of the true orbit [6]. However, the broadcast we 9 and 2007, errors ut curred on April 10, 4 contained an error of at least

6. Broadcast vs. precise GPS ephemerides: A historical perspective

D. L. M. Warren, J. F. Raquet

GPS Solutions, Vol. 7, issue (3), 2003-12

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made longer for these architectures as the ground monitoring is no longer responsible for meeting the 6.2 s TTA on its own.

Continuity requires that the above requirements be met continuously for the duration of the approach. Given that the above requirements are met at the initiation of the approach, the probability that one of them will be exceeded must be at or below 8×10^{-6} per 15 second interval [18], [24].

Availability is the fraction of time that all above requirements are met. For the system to be useful, it must be available at least 99% of the time at any location where LPV-200 service is authorized [18], [24]. For scheduled service, it may need to be available for even greater percentages of time (between 99.9% and 99.999%).

All of the architectures considered for this paper rely on dual frequency ranging measurements. The L1 and L5 signals are combined in a way to eliminate the first-order ionospheric delay [25]. Unfortunately, this combination increases the impact of measurement noise and multipath. The measurement noise term for the jth satellite can be described as normally distributed with zero mean and variance

$$\sigma^2_{j,\mathrm{DF_air}} = \left(\frac{f_1^2}{f_1^2 - f_5^2}\right)^2 \sigma^2_{\mathrm{L1},j,\mathrm{air}} + \left(\frac{f_5^2}{f_1^2 - f_5^2}\right)^2 \sigma^2_{\mathrm{L5},j,\mathrm{air}} \quad (1)$$

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$$\sigma_{j,{\rm{DF}}\_{\rm air}}^{2}=\left({f_{1}^{2}\over f_{1}^{2}-f_{5}^
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-f_{5}^{2}}\right)^{2}\sigma_{{\rm L5},j,{\rm air}}^{2} \eqno{\hbox{(1)}}$$
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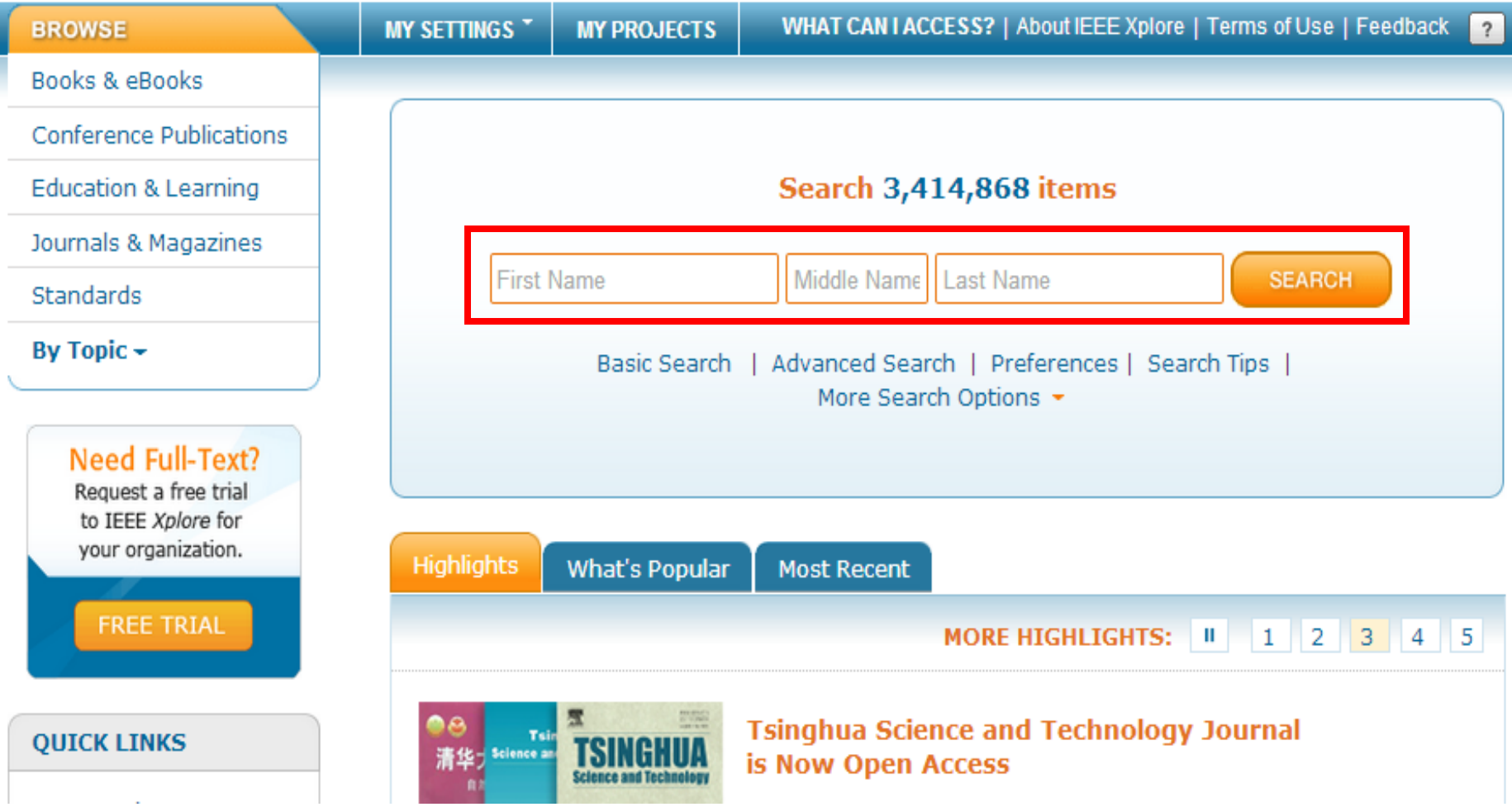
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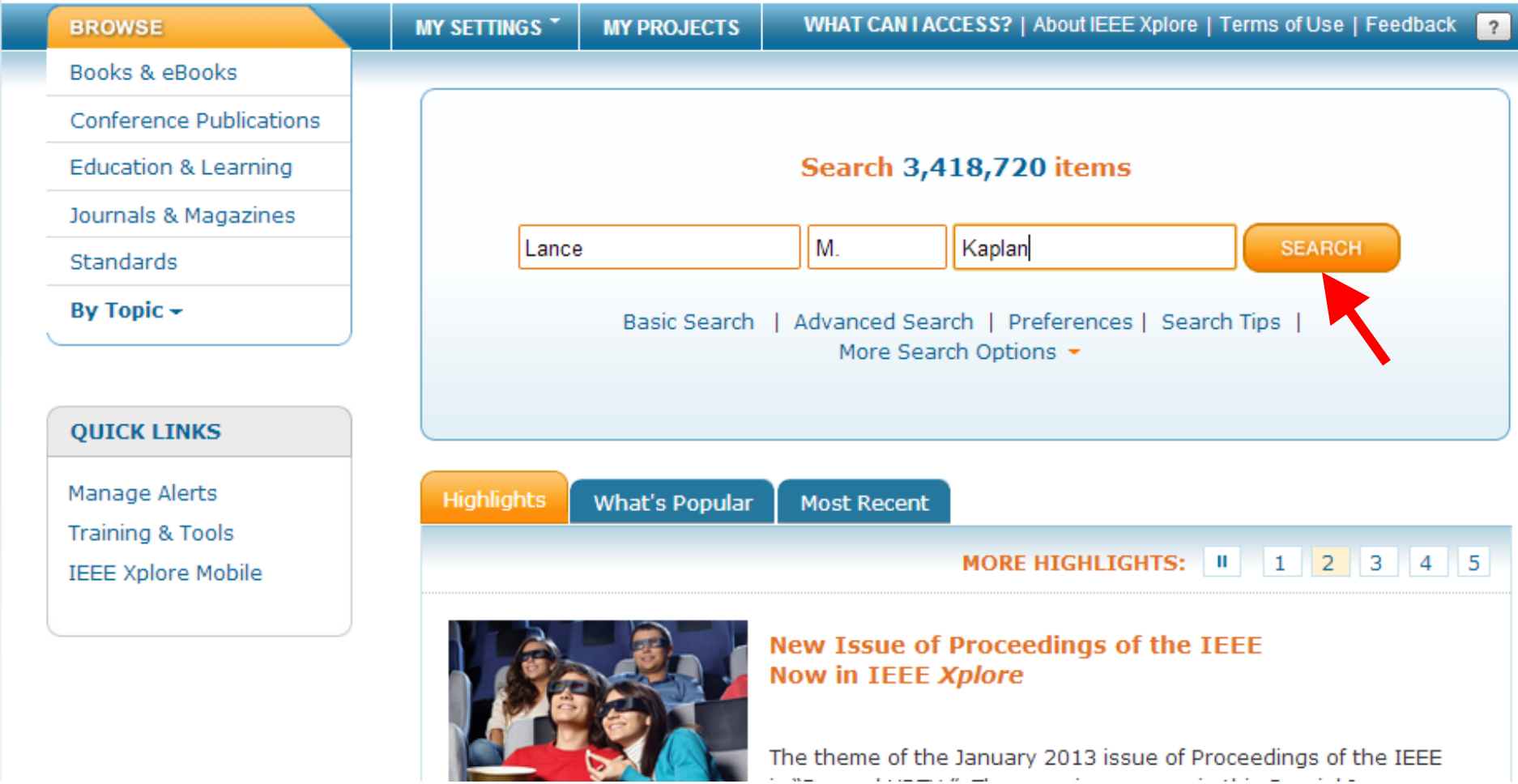
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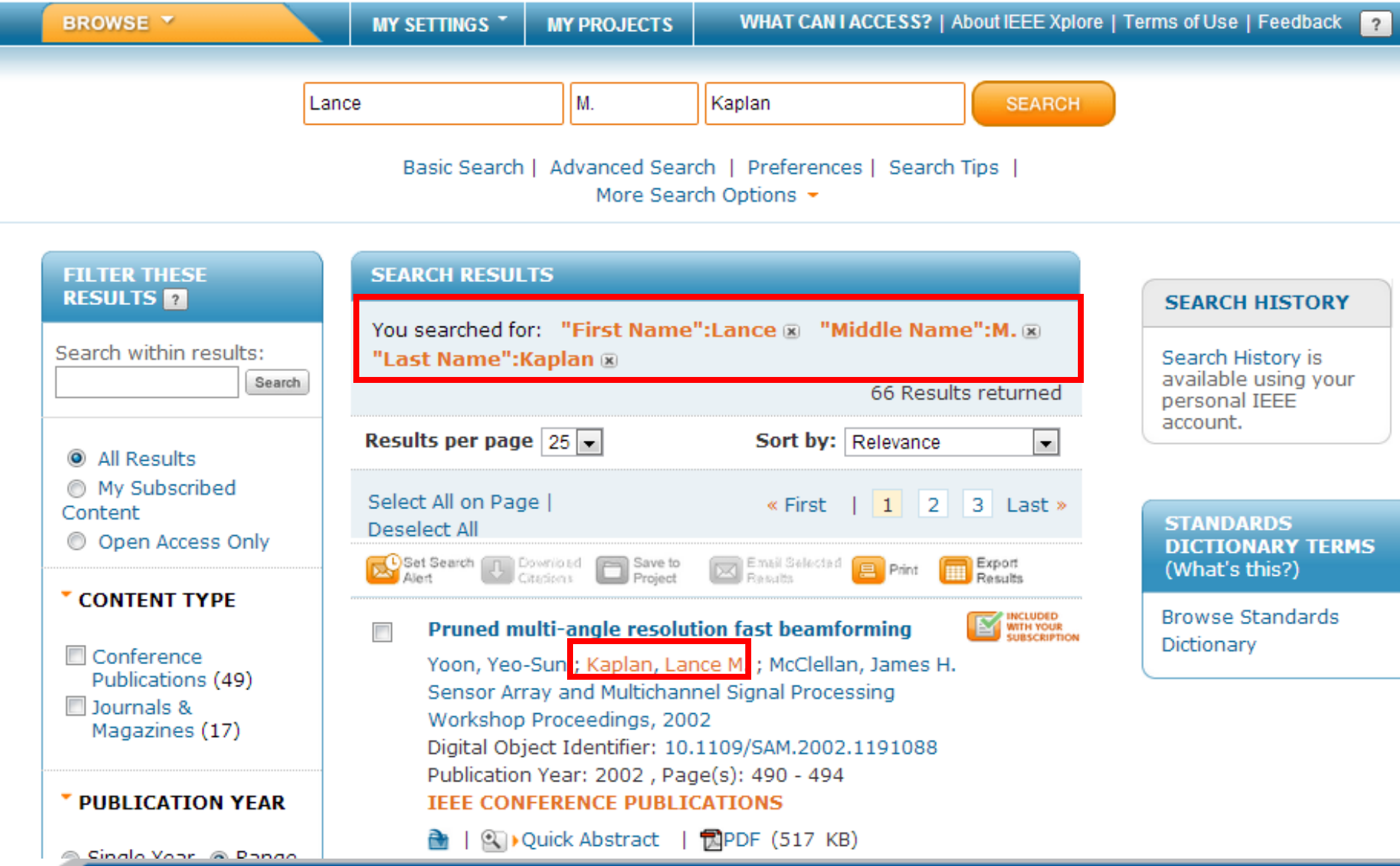
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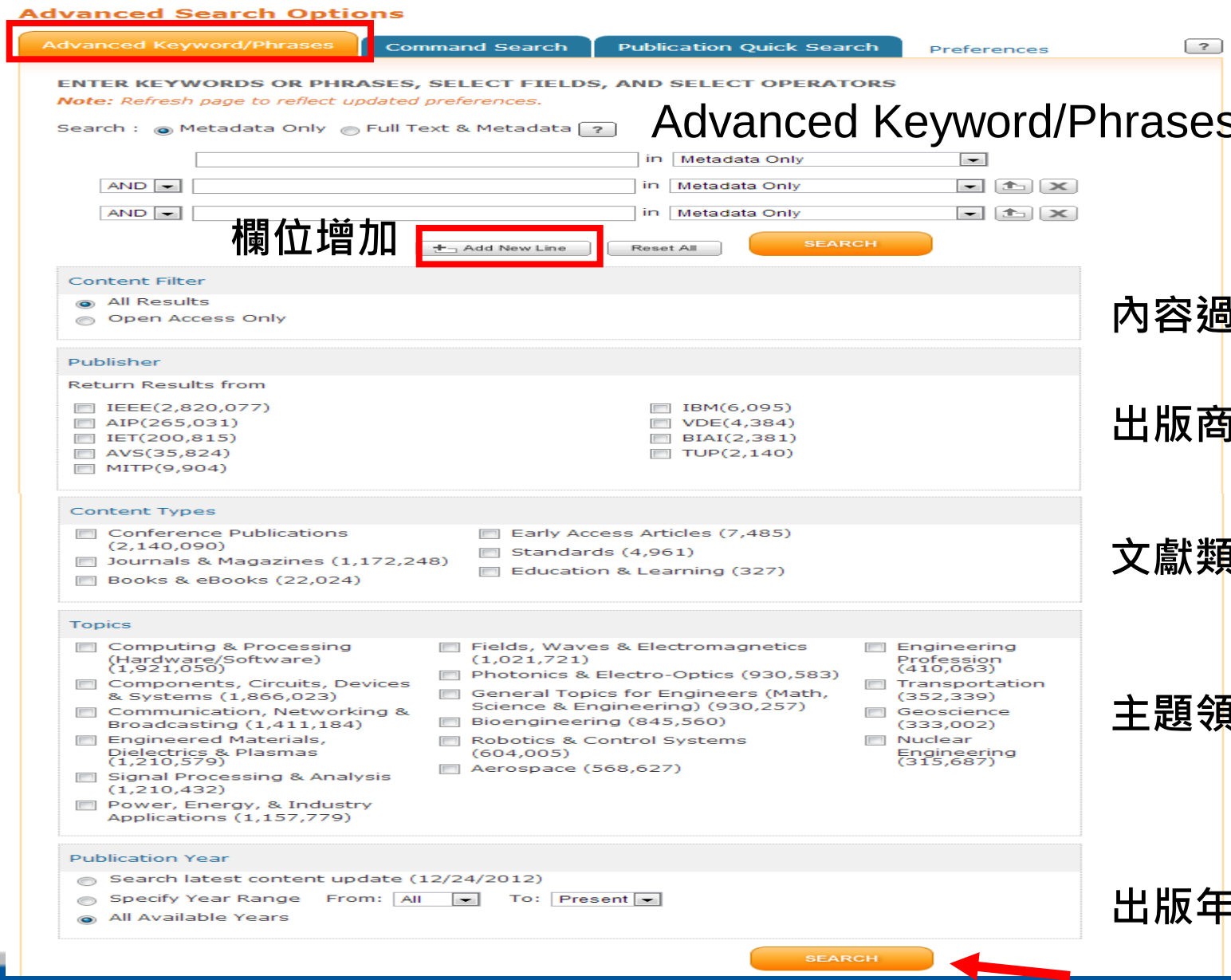

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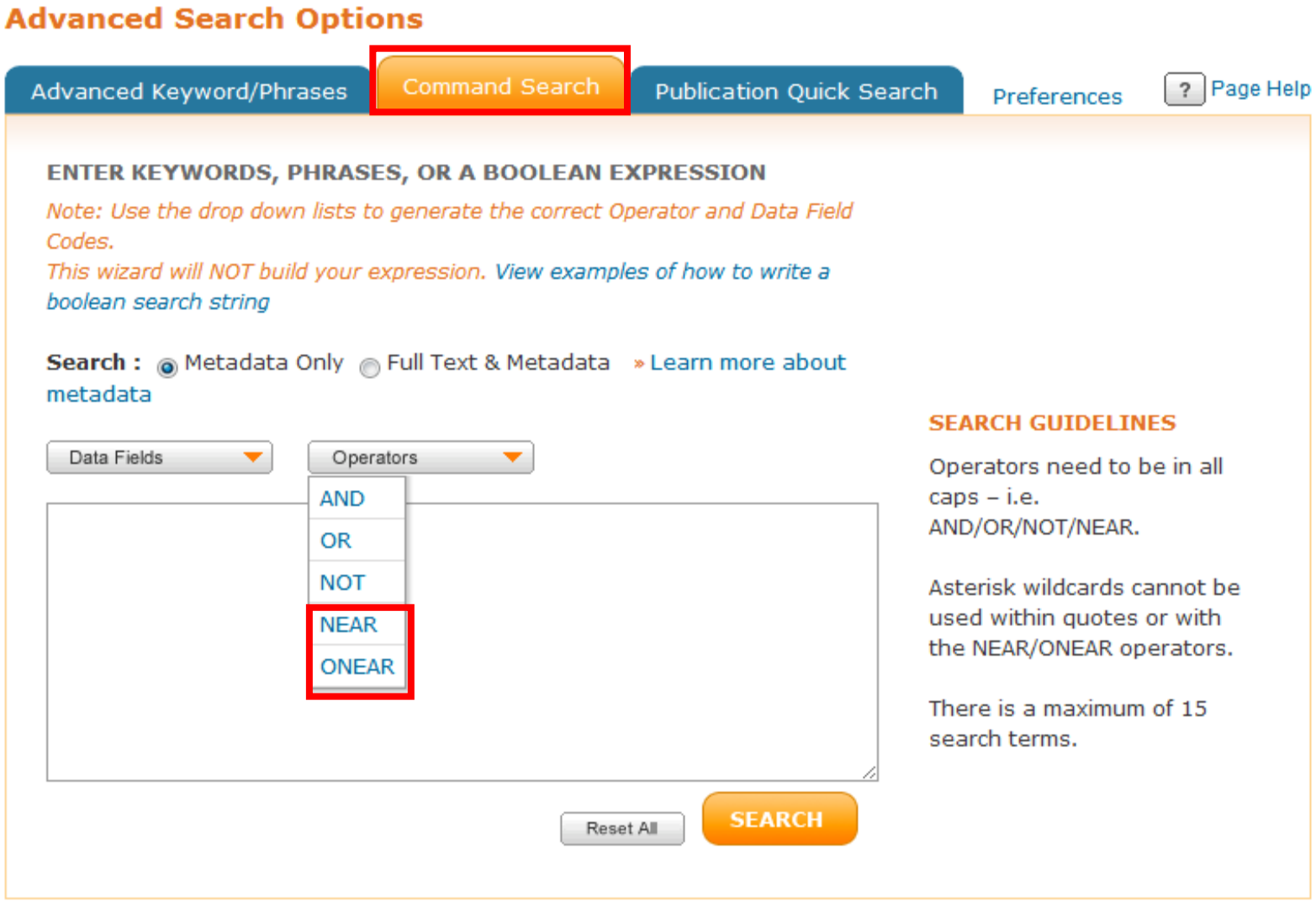
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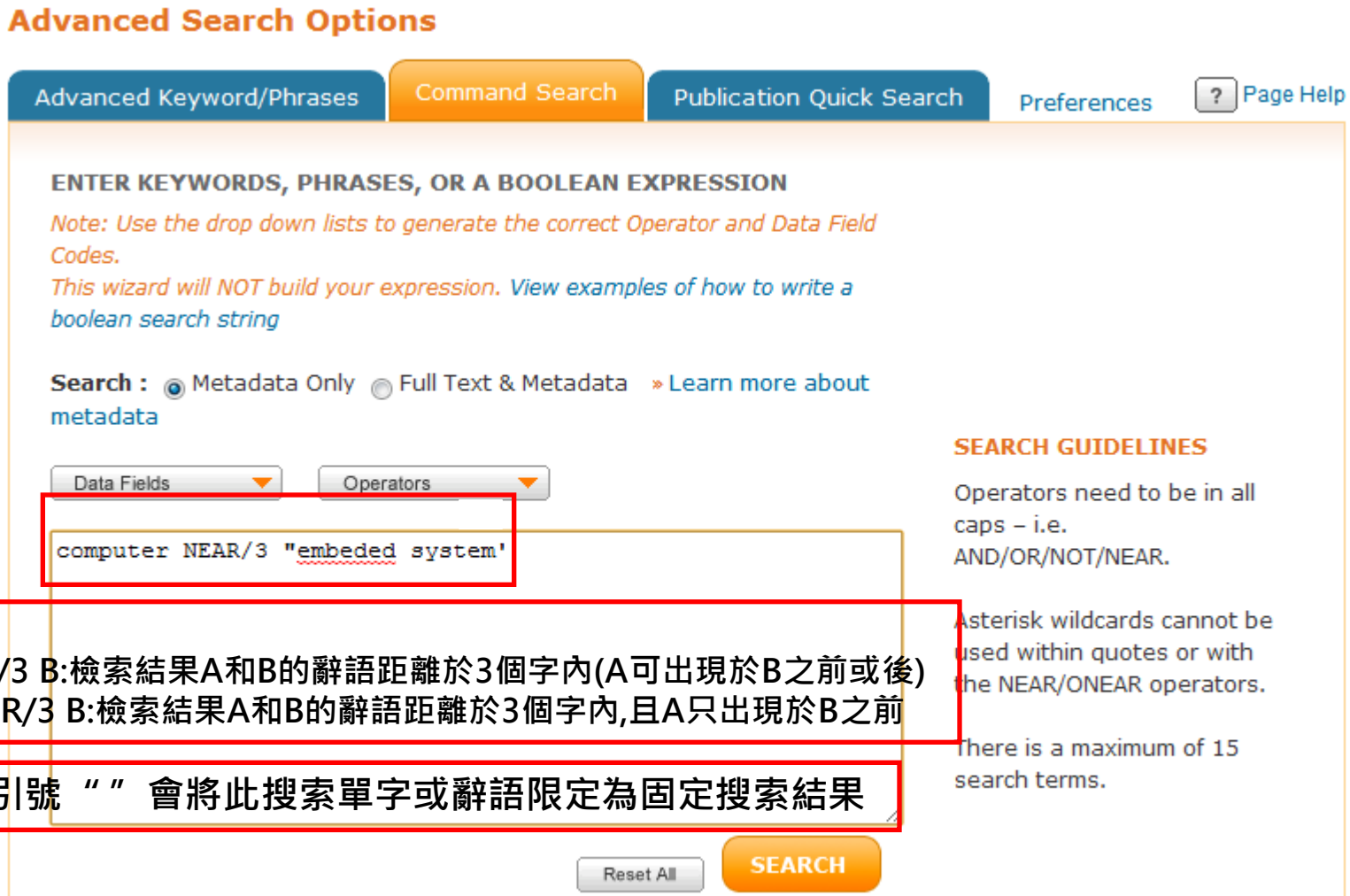

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A ONEAR/3 B:檢索結果A和B的辭語距離於3個字內,且A只出現於B之前

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Tips 檢索技巧 說明

Search #1: database AND query NEAR/3 optimiz* 無法顯示搜尋結果

- Wildcard(*)無法與proximity searches (NEAR/ONEAR)一起使用
- 只有在命令檢索中才能使 proximity searches
- 以下為命令檢索建議搜尋方式：

database AND ((query NEAR/3 optimize) OR (query NEAR/3 optimization))

Note: optimize will pick up optimize, optimizes, optimized, and optimizing. It will also pick up British spelling variations (optimize picks up optimise, optimises, etc.)

Search #2: database AND "query optimi*"無法顯示搜尋結果

- Wildcard(*)無法與雙引號一起使用
- 結合雙引號和OR運算符號來搜尋所有可能的結果

"query optimize" OR "query optimizing" OR "query optimization"

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Design and Implementation of Embedded SNMP Network Management Manager in Web-Based Mode

Quick Abstract

Xinyi Wu; Yong Zhu; Xiaolong Deng;

Asia-Pacific Services Computing Conference, 2008. APSCC '08. IEEE

Digital Object Identifier: 10.1109/APSCC.2008.151

Publication Year: 2008 , Page(s): 1512 - 1516

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AbstractPlus | Full Text: PDF (314 KB)

With the rapid development o computer technology, the embedded system has widely applied in many industrial fields. And these applications made it possible that the traditional personal-computer central pattern of computer transferred into the embedded-device central pattern. Nowadays, embedding Web-based SNMP manager into embedded devices is a hotspot of network management study with great significance. In this paper, we present the architecture of embedded network management based on SNMP agent and Web mode in Linux. It will enforce the abundant functions of network management in SNMP mode and expand the application scope of embedded-device in limited resource. Then we also draw a conclusion of critical research technology and developing policy

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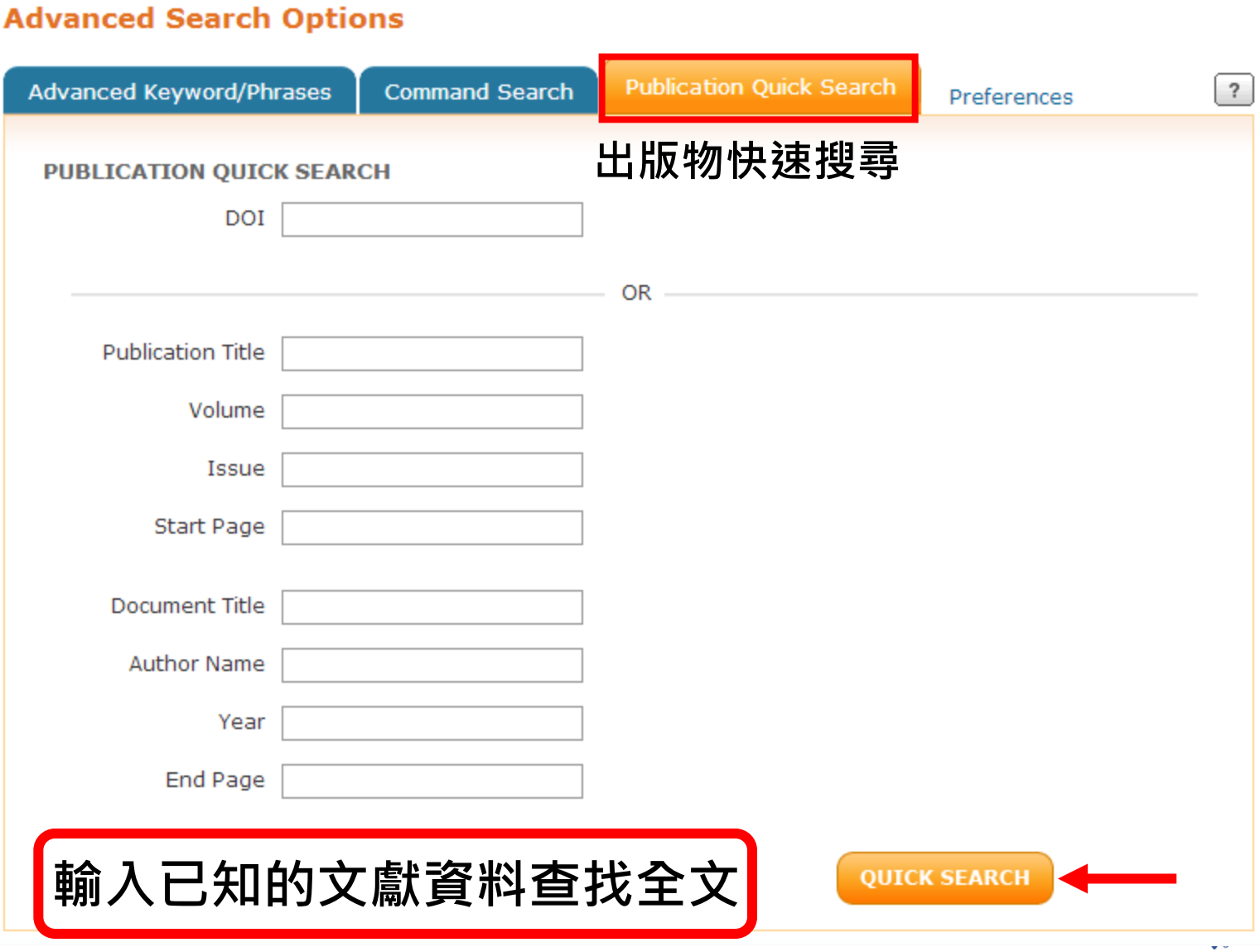

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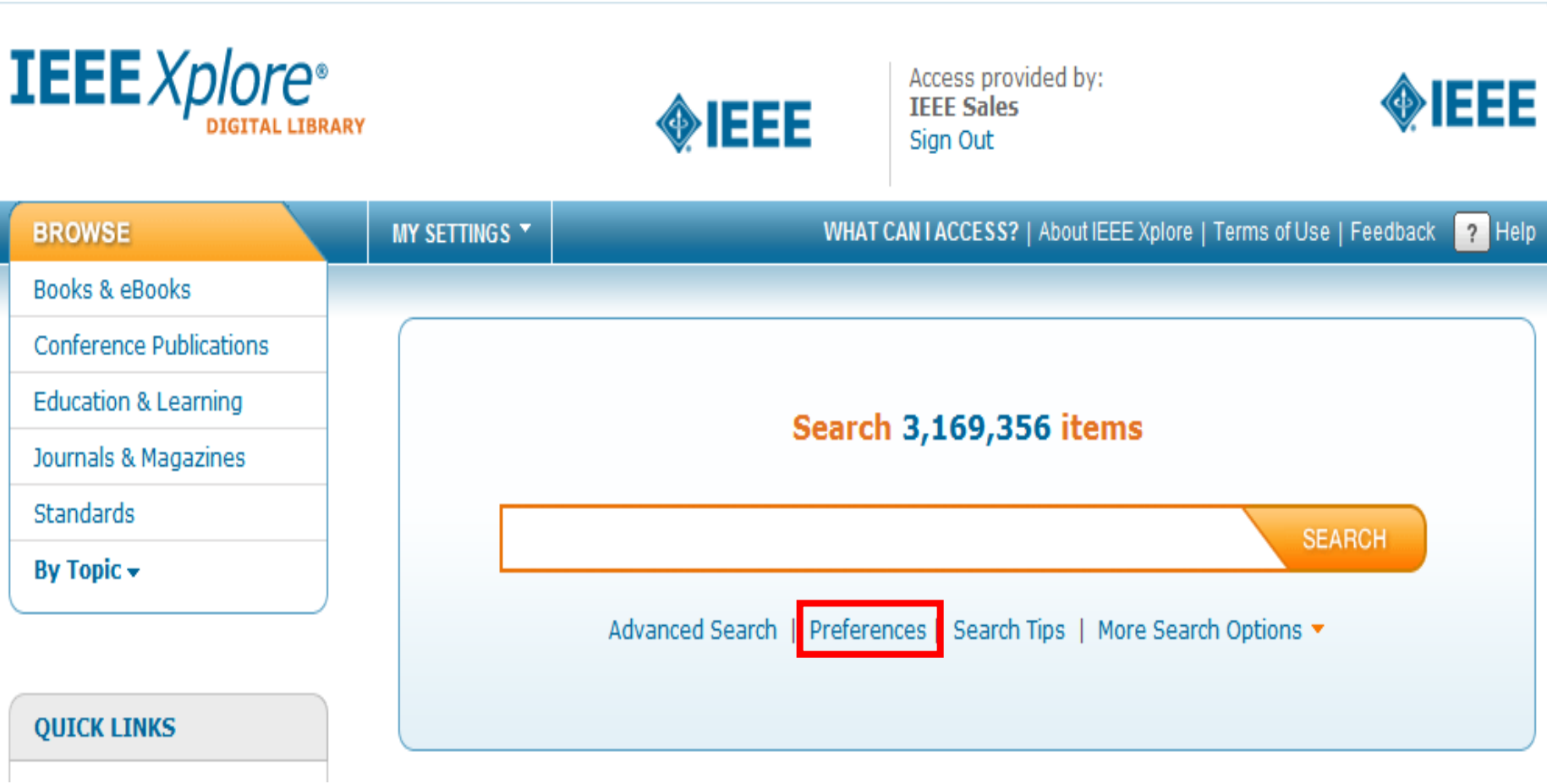

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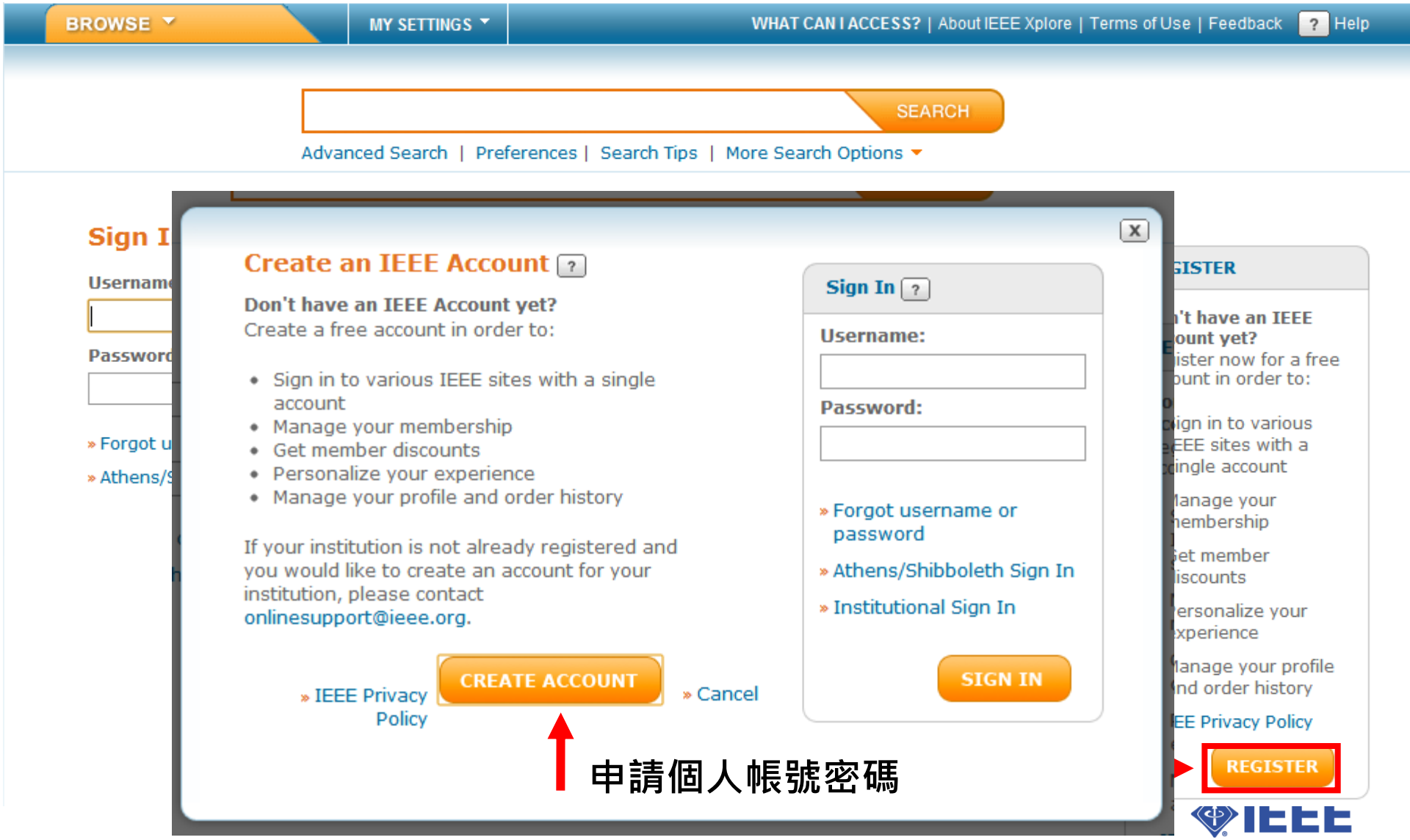

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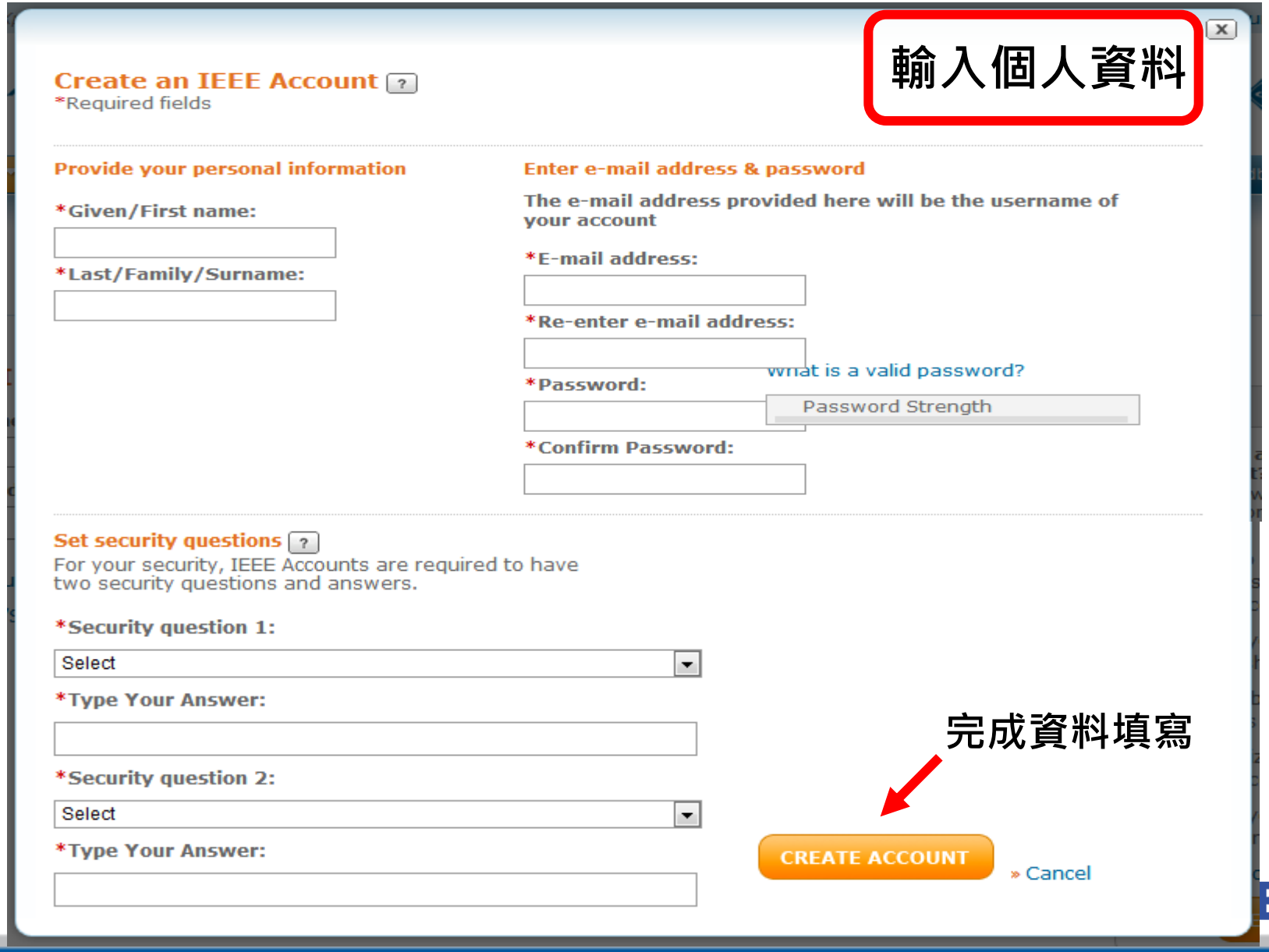

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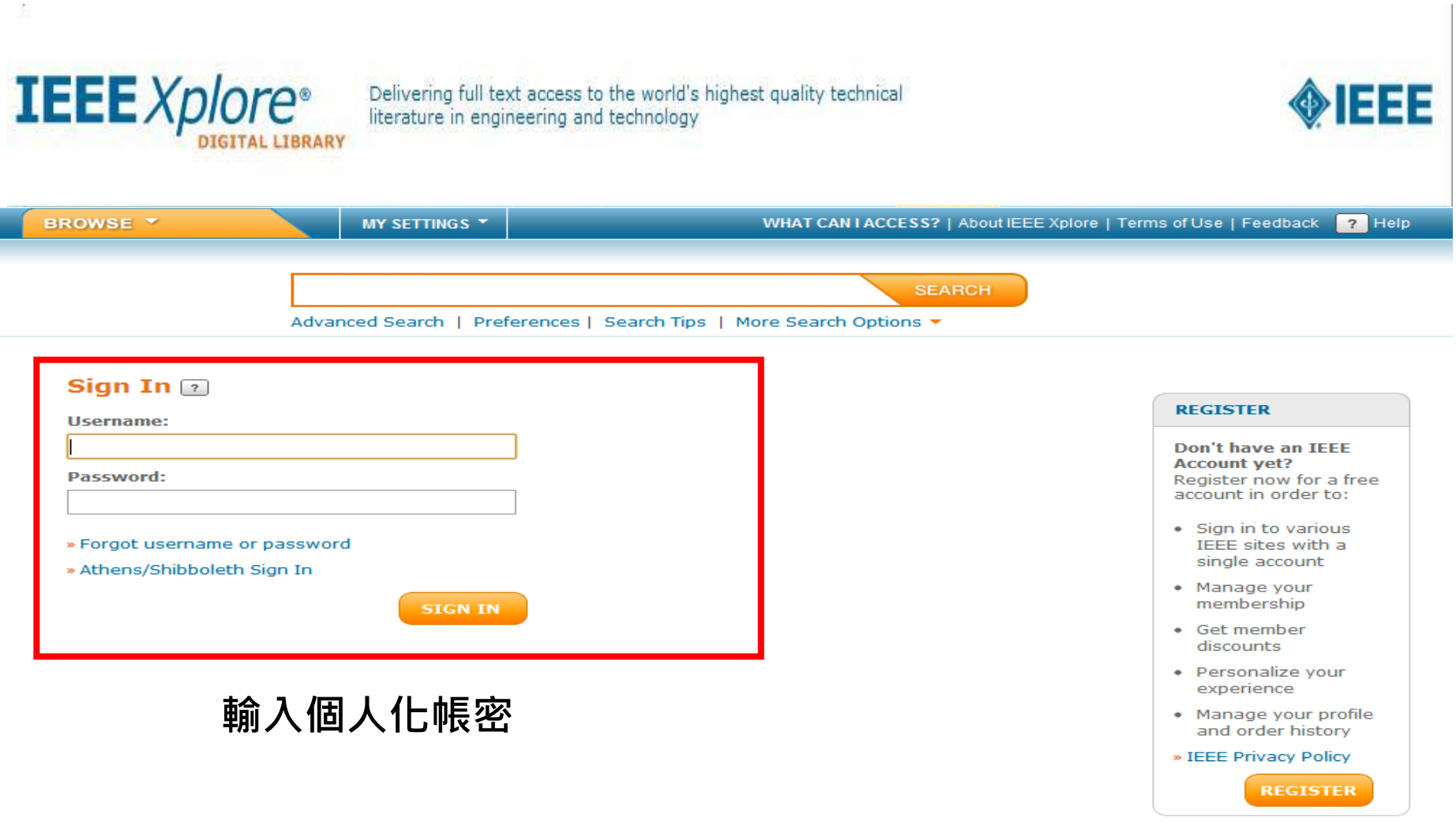

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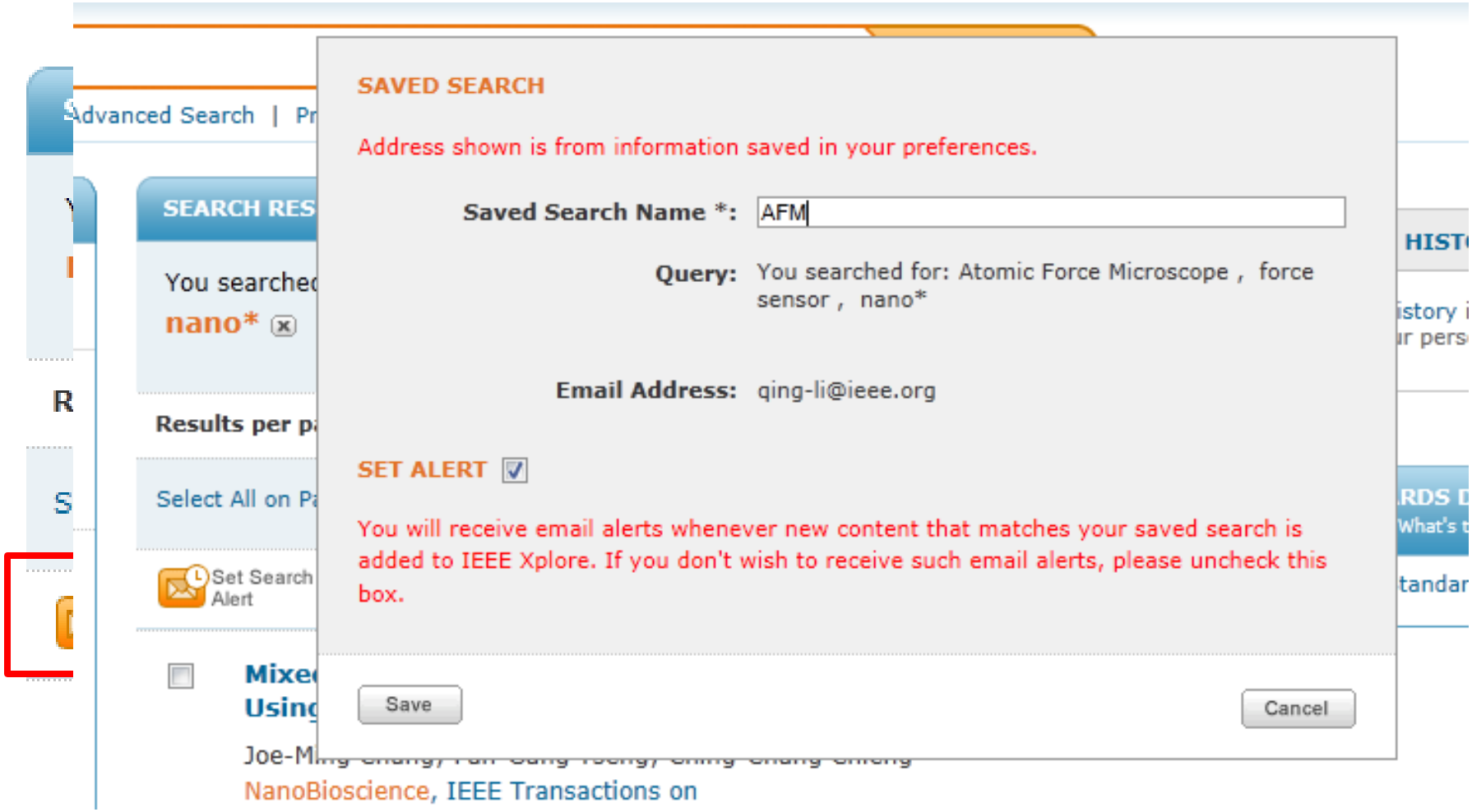
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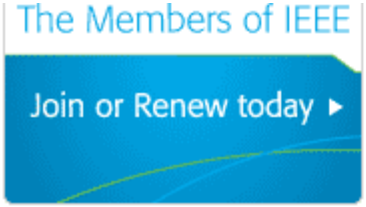

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- IEEE P11073-10441/D09, August 2012
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- ISO/IEC/IEEE P29119-1/DIS, September 2012

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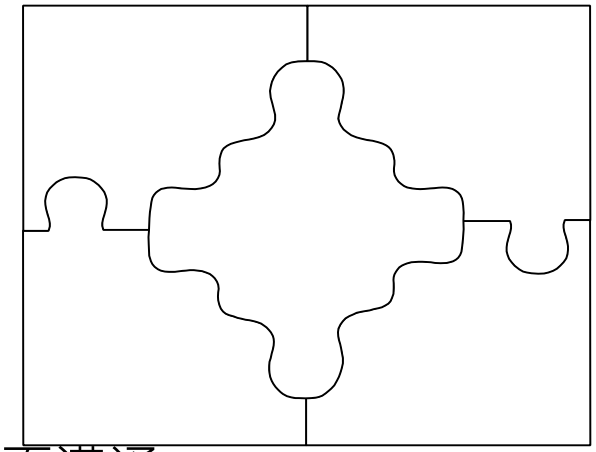

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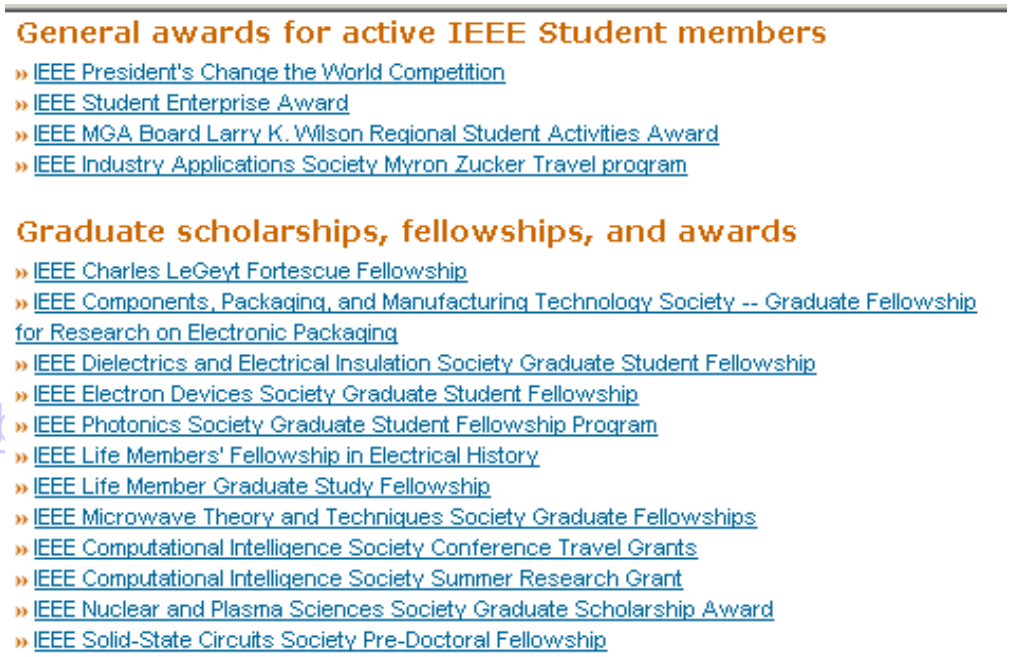

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  - IEEE*Xtreme* is a worldwide contest in which teams of students compete over a 24-hour time span against each other to solve a set of programming problems
- Who participates?
  - Teams of 2-3 students/graduate students and their proctors
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  - Worldwide, simultaneous!
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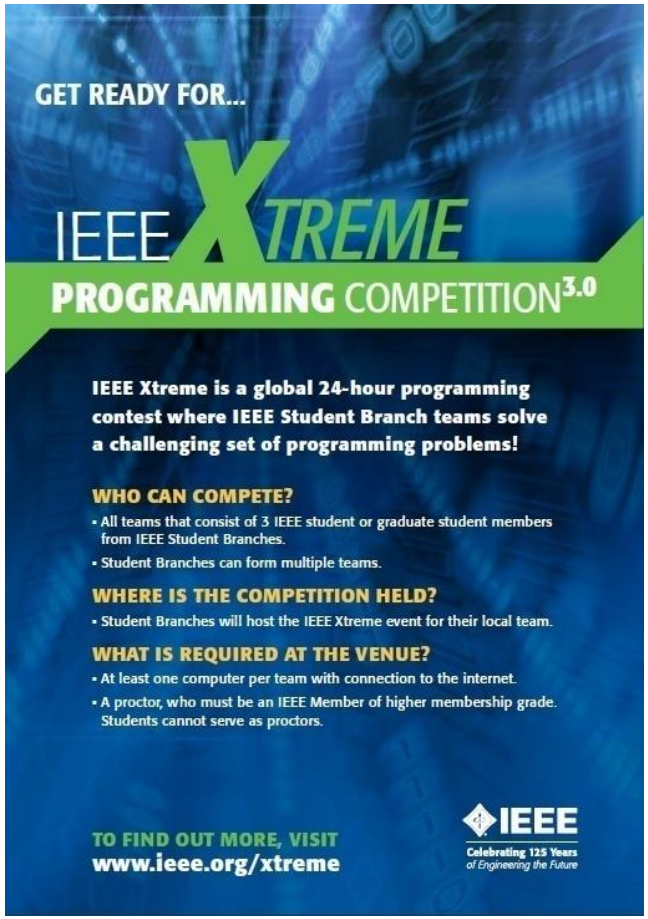

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